

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 6,912,680 B1
APPLICATION NO. : 08/798227
DATED : June 28, 2005
INVENTOR(S) : Brent Keeth

Page 1 of 3

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

| <u>Column, Line</u> | <u>Reads</u> | <u>Should Read</u> |
|----------------------------|---------------------------|-----------------------------------|
| On Title page | | |
| Item (56), References | [Omitted references] | --5,463,337 10/1995 |
| Cited, U.S. Patent | | Leonowich |
| Documents | | 5,712,883 1/1998 Miller |
| | | 5,764,092 6/1998 Wada |
| | | et al. |
| | | 6,125,157 9/2000 |
| | | Donnelly-- |
| On Title page | | |
| Item (56), References | "Ppaer 21.2," | --Paper 21.2,-- |
| Cited, Other Publications, | | |
| Chapman Reference | | |
| Item (56), References | "Nov. 1996, pp .1715- | --Nov. 1996, pp. 1715- |
| Cited, Other Publications, | 1722." | 1722.-- |
| von Kaenel Reference | | |
| Column 1, Line 13 | "must tightly controlled" | --must be tightly controlled-- |
| Column 1, Line 25 | "specified clock edge" | --specified clock edge,-- |
| Column 1, Line 30 | "propogation of delays." | --propogation delays.-- |

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| <u>Column, Line</u> | <u>Reads</u> | <u>Should Read</u> |
|---------------------|---------------------------------------|--|
| Column 2, Line 7 | "versions of the maser clock" | --versions of the master clock-- |
| Column 3, Line 13 | "with edges the" | --with edges of the-- |
| Column 4, Line 30 | "and fine venires 96" | --and fine verniers 96-- |
| Column 5, Line 63 | "timing in not excessive," | --timing is not excessive,-- |
| Column 6, Line 4 | "the new course" | --the new coarse-- |
| Column 6, Line 40 | "output timing:" | --output timing comprises:-- |
| Column 7, Line 15 | "time delay adjusting" | --time delay comprises adjusting-- |
| Column 7, Line 45 | "comparator:" | --comparator comprises:-- |
| Column 7, Line 49 | "a pluarlity of phase comparator," | --a plurality of phase comparators,-- |

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| <u>Column, Line</u> | <u>Reads</u> | <u>Should Read</u> |
|---------------------|---------------------------------------|--|
| Column 8, Line 19 | "a pluarlity of phase comparator," | --a plurality of phase comparators,-- |

Signed and Sealed this

Twenty-ninth Day of May, 2007



JON W. DUDAS
Director of the United States Patent and Trademark Office